

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
24 June 2004 (24.06.2004)

PCT

(10) International Publication Number
WO 2004/053944 A3

(51) International Patent Classification⁷: **H01L 21/20**,
21/44, 21/66, G01R 1/04, 19/00, 31/26, 31/28

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(21) International Application Number:
PCT/US2003/039698

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR,
CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD,
GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR,
KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN,
MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU,
SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA,
UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(22) International Filing Date:
11 December 2003 (11.12.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/432,786 11 December 2002 (11.12.2002) US

(84) Designated States (*regional*): ARIPO patent (BW, GH,
GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE,
SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA,
GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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Declaration under Rule 4.17:

— *of inventorship (Rule 4.17(iv)) for US only*

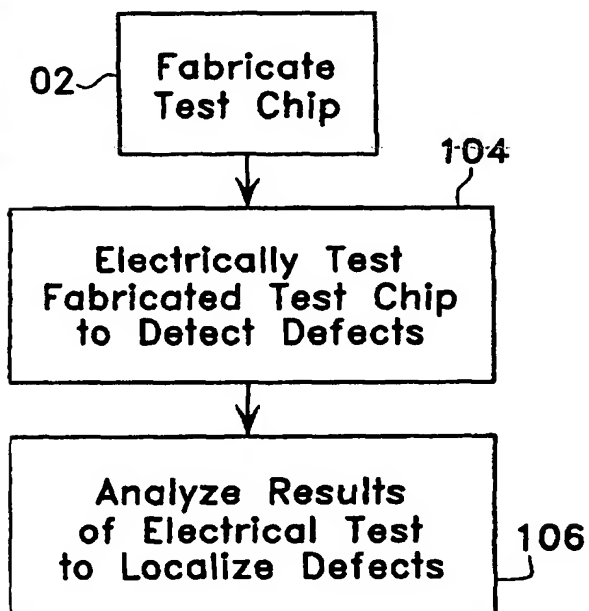
Published:

— *with international search report*

(88) Date of publication of the international search report:
4 November 2004

[Continued on next page]

(54) Title: FAST LOCALIZATION OF ELECTRICAL FAILURES ON AN INTEGRATED CIRCUIT SYSTEM AND METHOD



(57) Abstract: Fast localization of electrically measured defects of integrated circuits includes providing information for fabricating a test chip having test structures configured for parallel electrical testing. The test structures on the test chip are electrically tested employing a parallel electrical tester. The results of the electrical testing are analyzed to localize defects on the test-chip.

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/US03/39698

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H01L 21/20, 21/44, 21/66; G01R 1/04, 19/00, 31/26, 31/28
US CL : 438/14, 17, 18, 19, 20; 324/158.1, 763, 765; 257/48

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 438/14, 17, 18, 19, 20; 324/158.1, 763, 765; 257/48

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
NONE

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 6,358,762 B1 (Kohno et al.) 19 March 2002 (19.03.2002), columns 1-14.	1, 5, 12, 16, 19, 29-32, 38
Y	US 4,912,052 (MIYOSHI et al.) 27 March 1990 (27.03.1990), Fig. 5, 7, 9.	27
Y	US 6,475,871 B1 (STINE et al.) 5 November 2002 (05.11.2002), columns 1-10.	1-58
Y	US 5,831,446 (SO et al.) 3 November 1998 (03.11.1998), column 1-8.	1
Y	US 6,291,254 B1 (CHOU et al.) 18 September 2001 (18.09.2001), columns 1-22.	2-6, 30, 31
Y	US 6,137,303 (DECKERT et al.) 24 October 2000 (24.10.2000), columns 1-14.	1-58
Y	US 6,313,652 B1 (MAENG) 6 November 2001 (06.11.2001), columns 1-18.	26-27

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

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"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

25 May 2004 (25.05.2004)

Date of mailing of the international search report

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